924 09/973019 10/10/01

u.s. ut	ILITY	PATENT	APPL	<u>ICATION</u>
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SCANNED HEM L GARAGE

PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER H. PHHALL
09/973019	F	257	500	2811	CHEROLD

Hiroshi Watanabe Kiyomi Naruke Kazunori Masuda

Semiconductor device and method of manufacturing the same

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PTO-2040 12/99

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION						
OPI	IGINAL	<u>-</u>	CROSS REFERENCE(S)			
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
- ULMOS	+					
INTERNATIONAL	L CLASSIFICATION					
						
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TERMINAL	DRAWINGS			CLAIMS ALLOWED	
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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a) The term of this patent subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)		
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